

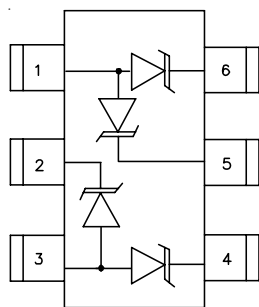
Description

The DL0524S2 is a low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The DL0524S2 has a very-low capacitance with a typical value at 1.0pF, and complies with the IEC 61000-4-2 (ESD) standard with $\pm 15\text{kV}$ air and $\pm 8\text{kV}$ contact discharge. It is assembled into a 6-pin lead-free SOT23-6 package. The low capacitance array make it ideal for four high speed data and transmission line. This device is optimized for ESD protection of portable electronics.

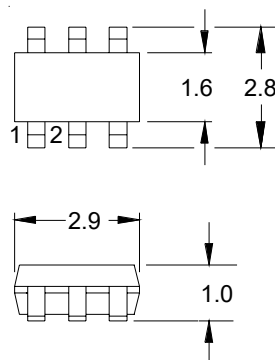
Mechanical Characteristics

- ◆ Package: SOT23-6
- ◆ Lead Finish: Matte Tin
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Case Material: "Green" Molding Compound
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below

Dimensions and Pin Configuration



Circuit Schematic



Package Outline

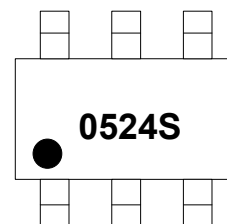
Features

- ◆ Ultra low capacitance: 1.0pF typical (I/O to I/O)
- ◆ Ultra low leakage: nA level
- ◆ Low operating voltage: 5V
- ◆ Low clamping voltage
- ◆ 4 data lines protects
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 15\text{kV}$
 - Contact discharge: $\pm 8\text{kV}$
 - IEC61000-4-4 (EFT) 40A (5/50ns)
 - IEC61000-4-5 (Lightning) : 6A(8/20 μs)
- ◆ ROHS Compliant

Applications

- ◆ PoE
- ◆ Digital video interface(DVI)
- ◆ Monitor and Flat Panel Displays
- ◆ Notebook Computer
- ◆ 10/100/1000 Ethernet

Marking Information



0524S = Device Marking Code

Dot denotes Pin1

Ordering Information

Part Number	Marking	Packaging	Reel Size
DL0524S2	0524S	3000/Tape & Reel	7 inch

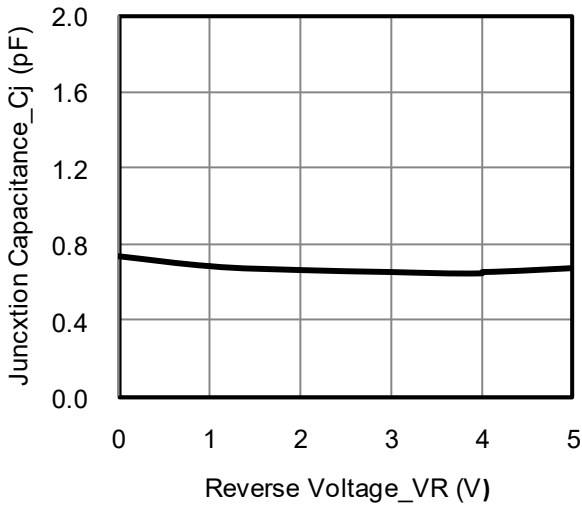
Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	150	W
Peak Pulse Current (8/20 μs)	I _{PP}	6	A
ESD per IEC 61000-4-2 (Air)	V _{ESD}	±15	kV
ESD per IEC 61000-4-2 (Contact)		±8	
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

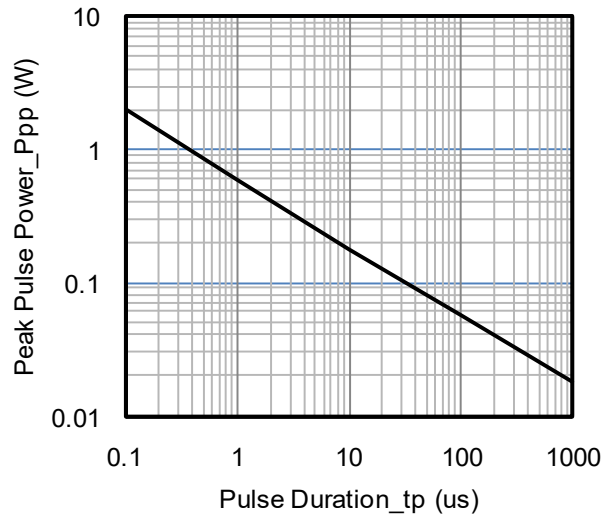
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			5	V	Pin 2 or 4 to Pin 3, Pin 5 or 6 to
Breakdown Voltage	V _{BR}	6			V	I _T = 1mA, Pin 2 or 4 to Pin 3, Pin 5 or 6 to Pin 1
Reverse Leakage Current	I _R			0.5	μA	V _{RWM} = 5V, Pin 2 or 4 to Pin 3, Pin 5 or 6 to Pin 1
Clamping Voltage	V _C			15	V	I _{PP} = 1A (8 x 20 μs pulse), Pin 2 or 4 to Pin 3, Pin 5 or 6 to Pin 1
Clamping Voltage	V _C			24	V	I _{PP} = 6A (8 x 20 μs pulse), Pin 2 or 4 to Pin 3, Pin 5 or 6 to Pin 1
Clamping Voltage	V _C			26	V	I _{PP} = 6A (8 x 20 μs pulse), Pin 2 to Pin 4, Pin 5 to Pin 6
Junction Capacitance	C _J			5	pF	V _R = 0V, f = 1MHz, any I/O pin to ground
Junction Capacitance	C _J		1	3	pF	V _R = 0V, f = 1MHz, between I/O pins

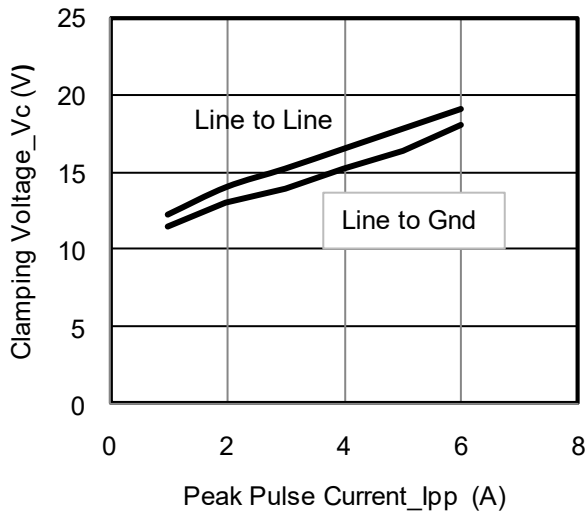
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



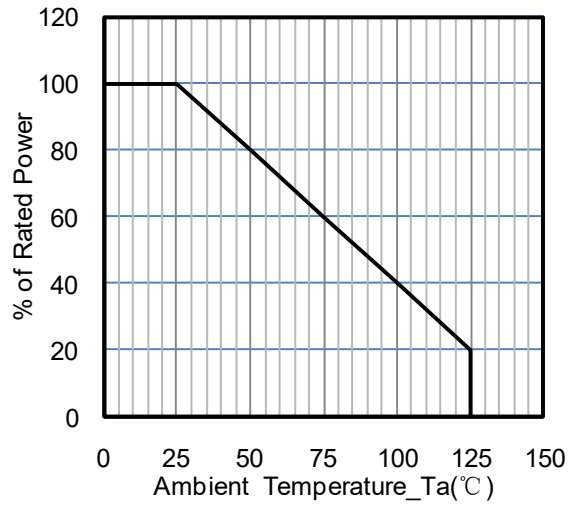
Junction Capacitance vs. Reverse Voltage



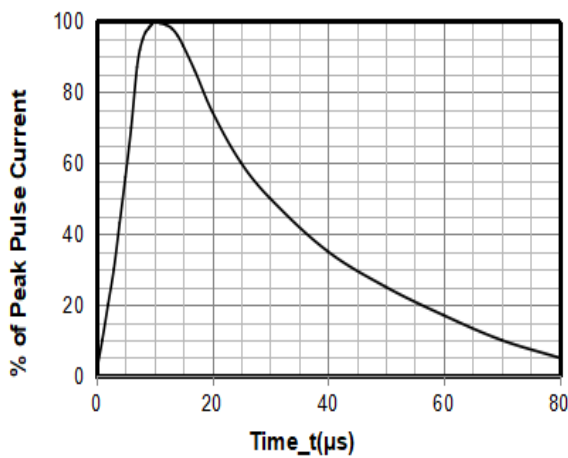
Peak Pulse Power vs. Pulse Time



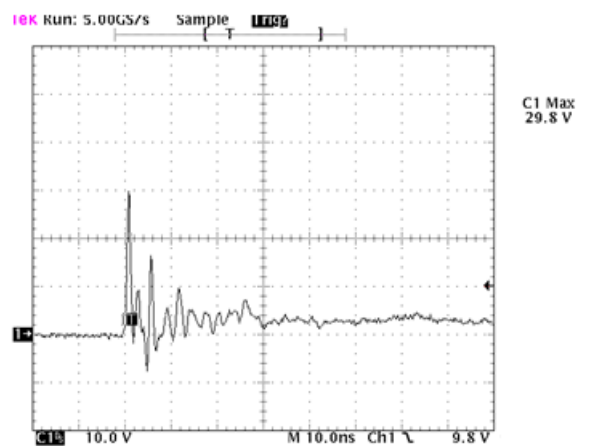
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve

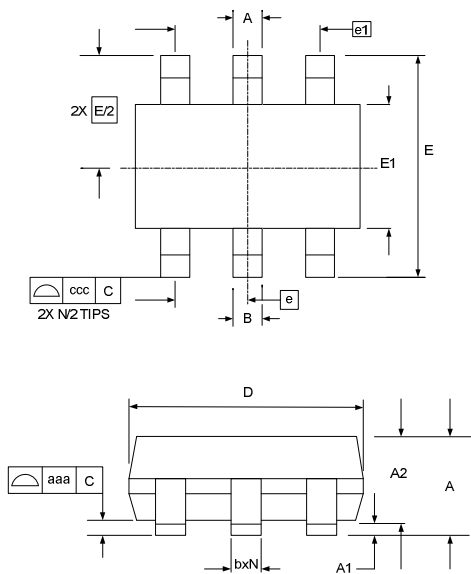


8 X 20uS Pulse Waveform



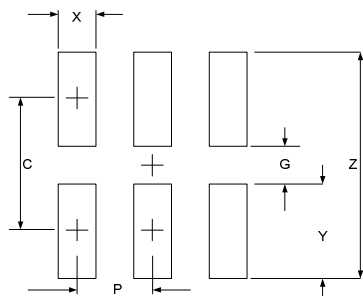
**ESD Clamping Voltage
8 kV Contact per IEC61000-4-2**

SOT23-6 Package Outline Drawing



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90		1.45	0.035		0.057
A1	0.00		0.15	0.000		0.006
A2	0.90	1.15	1.30	0.035	0.045	0.051
b	0.25		0.50	0.010		0.020
c	0.08		0.22	0.003		0.009
D	2.80	2.90	3.10	0.110	0.114	0.122
E1	1.50	1.60	1.75	0.060	0.063	0.069
E	2.80 BSC			0.110 BSC		
e	0.95 BSC			0.037 BSC		
e1	1.90 BSC			0.075 BSC		
N	6			6		
aaa	0.10			0.004		
ccc	0.20			0.008		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
C	2.50	0.098
G	1.40	0.055
P	0.95	0.037
X	0.60	0.024
Y	1.10	0.043
Z	3.60	0.141

Contact Information

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